

FIG. 1
(Prior Art)

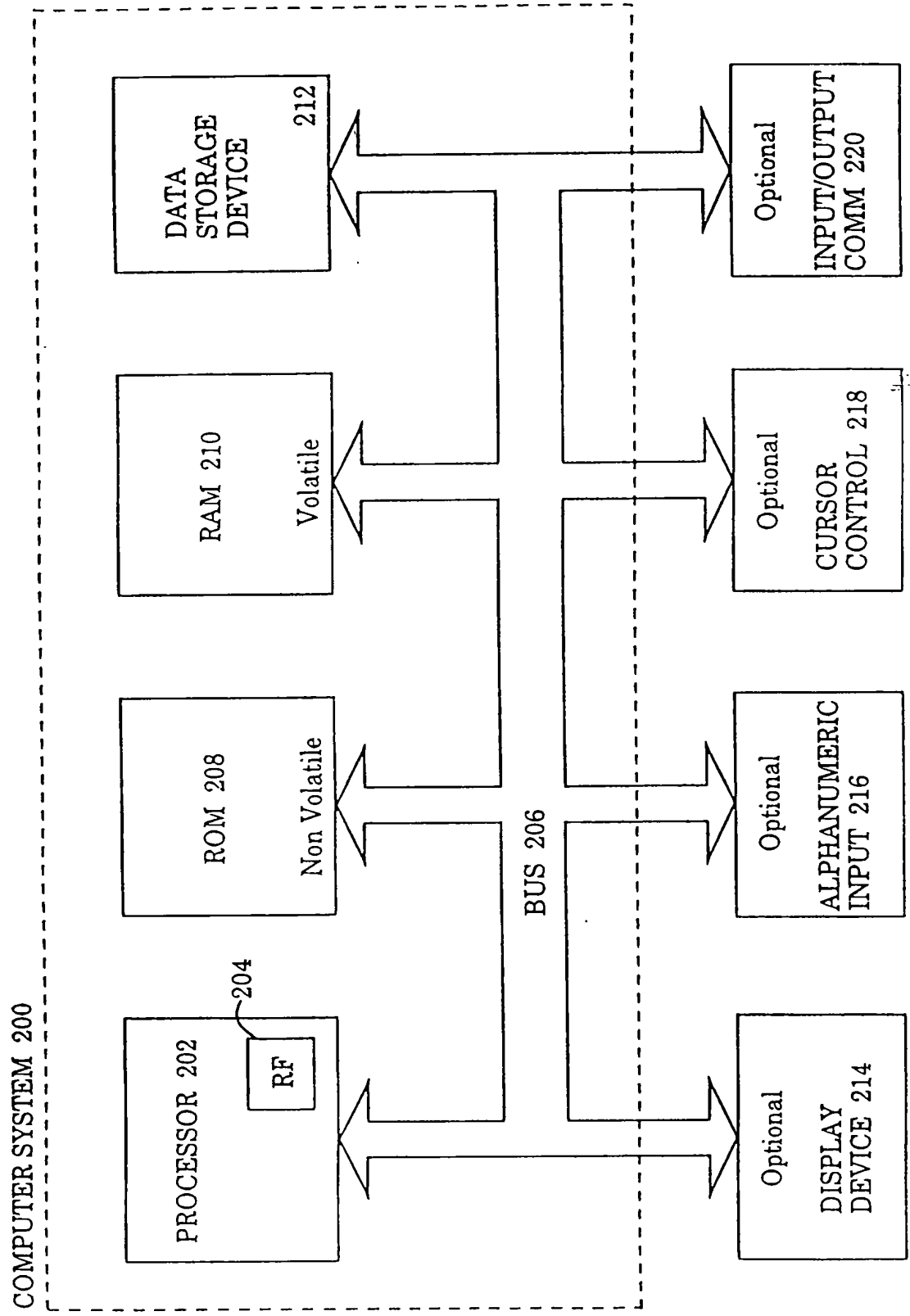


FIG. 2 (Prior Art)

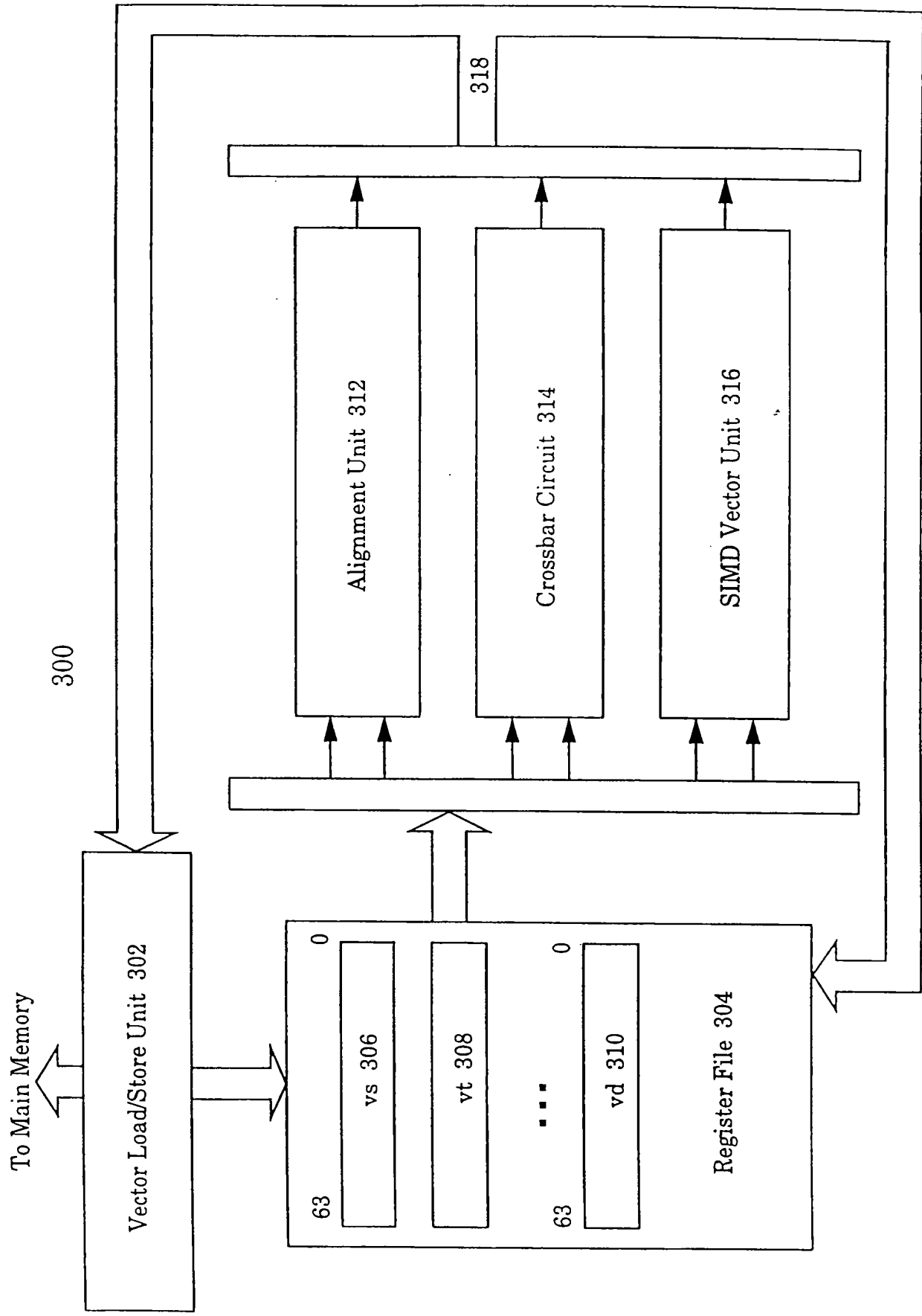


FIG. 3

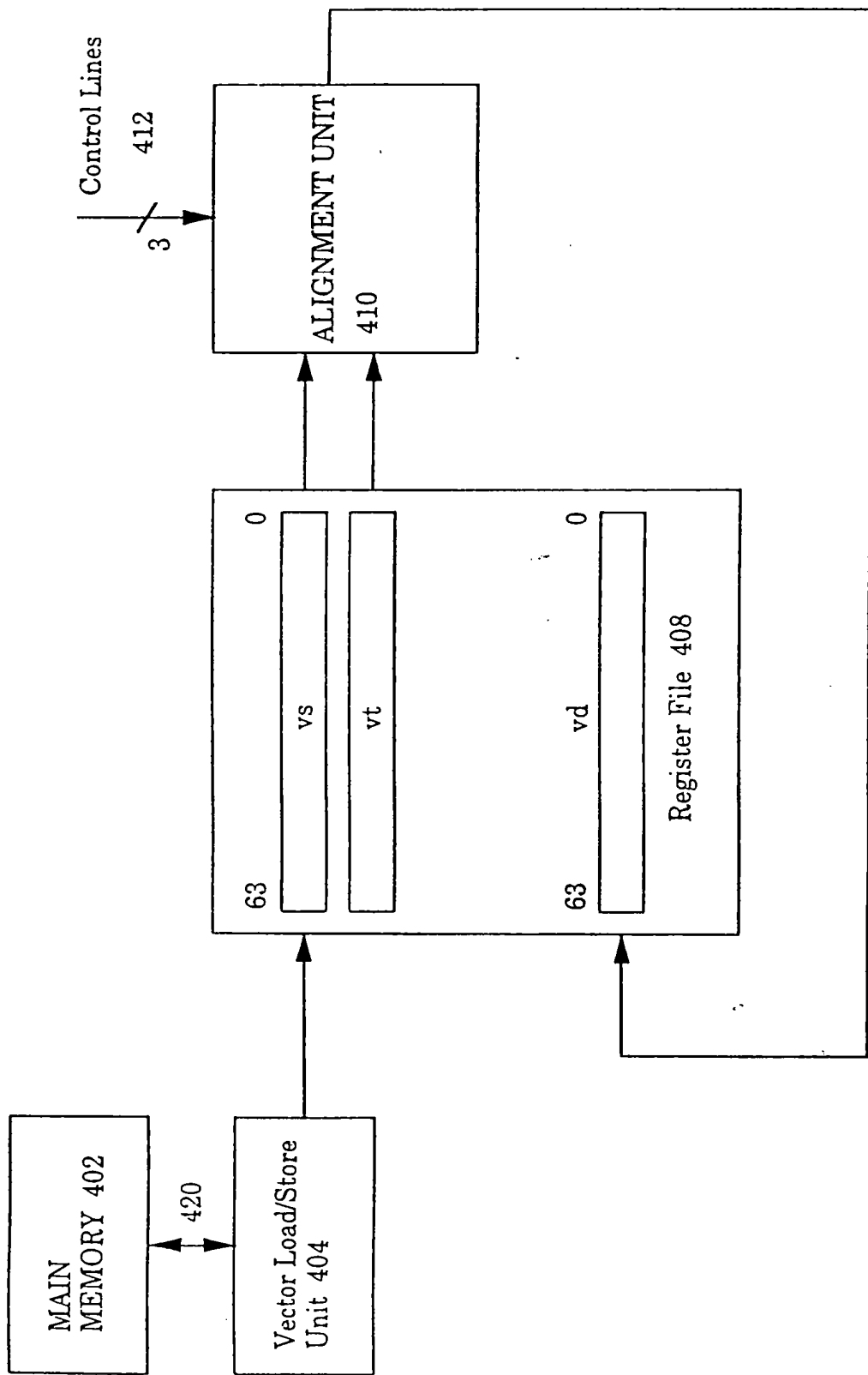


FIG. 4

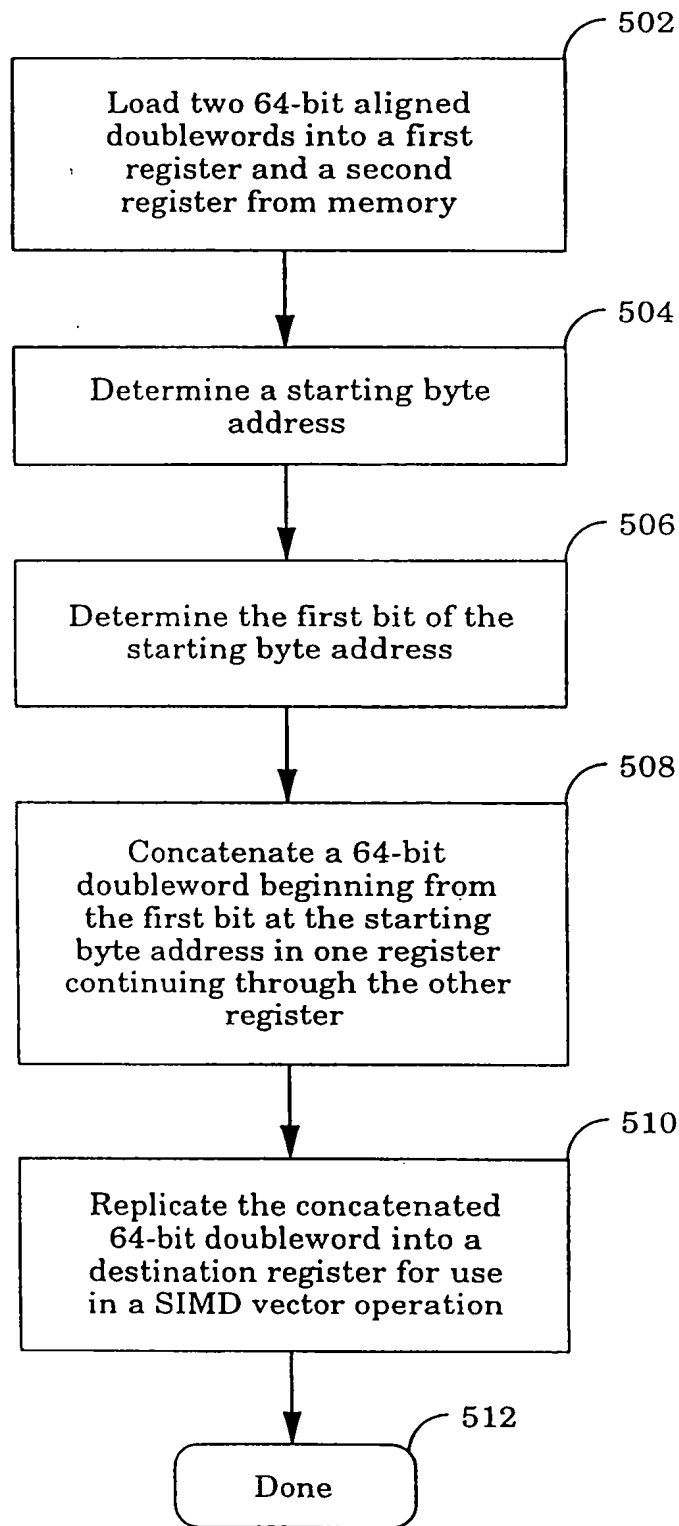


FIG. 5

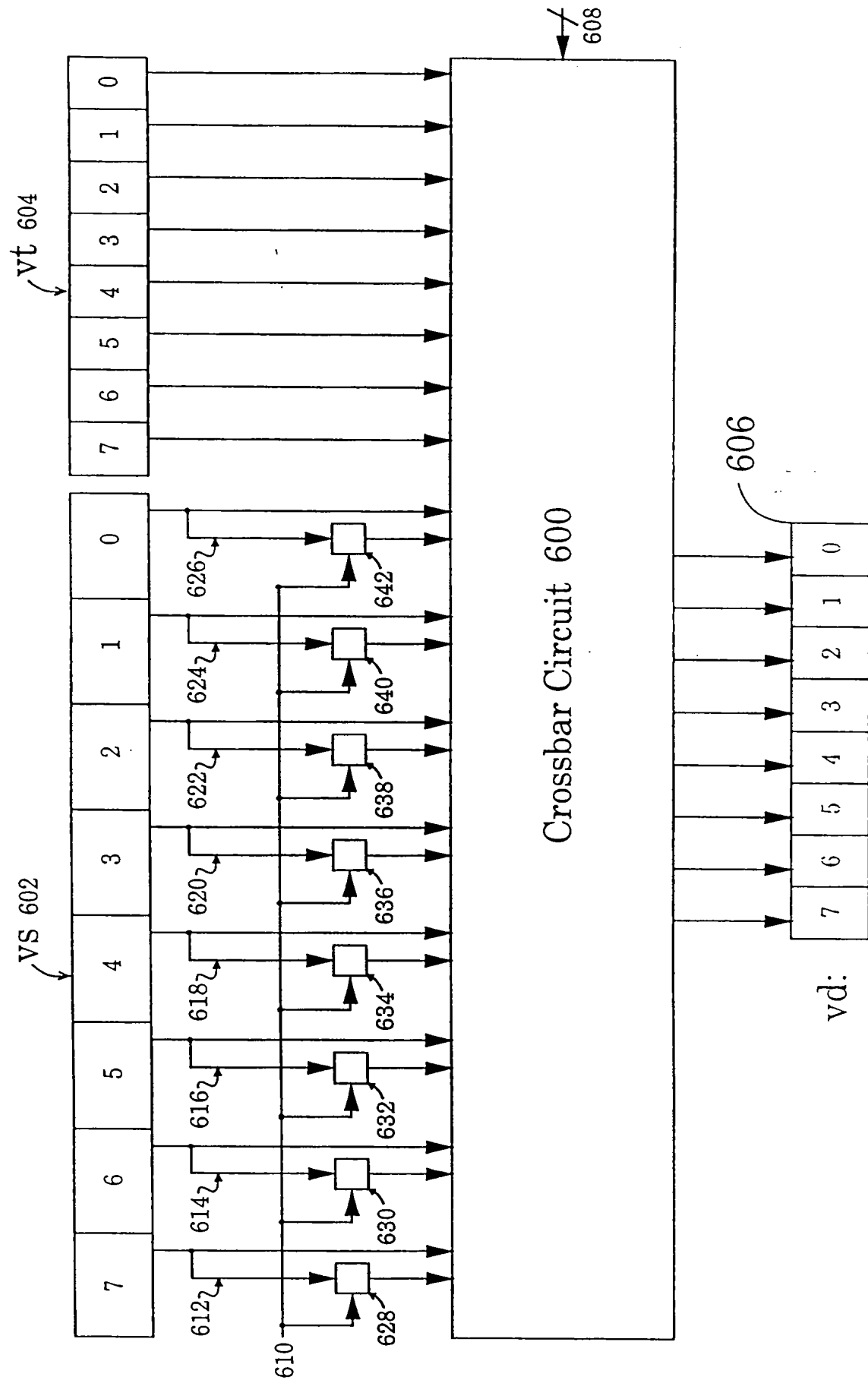


FIG. 6A

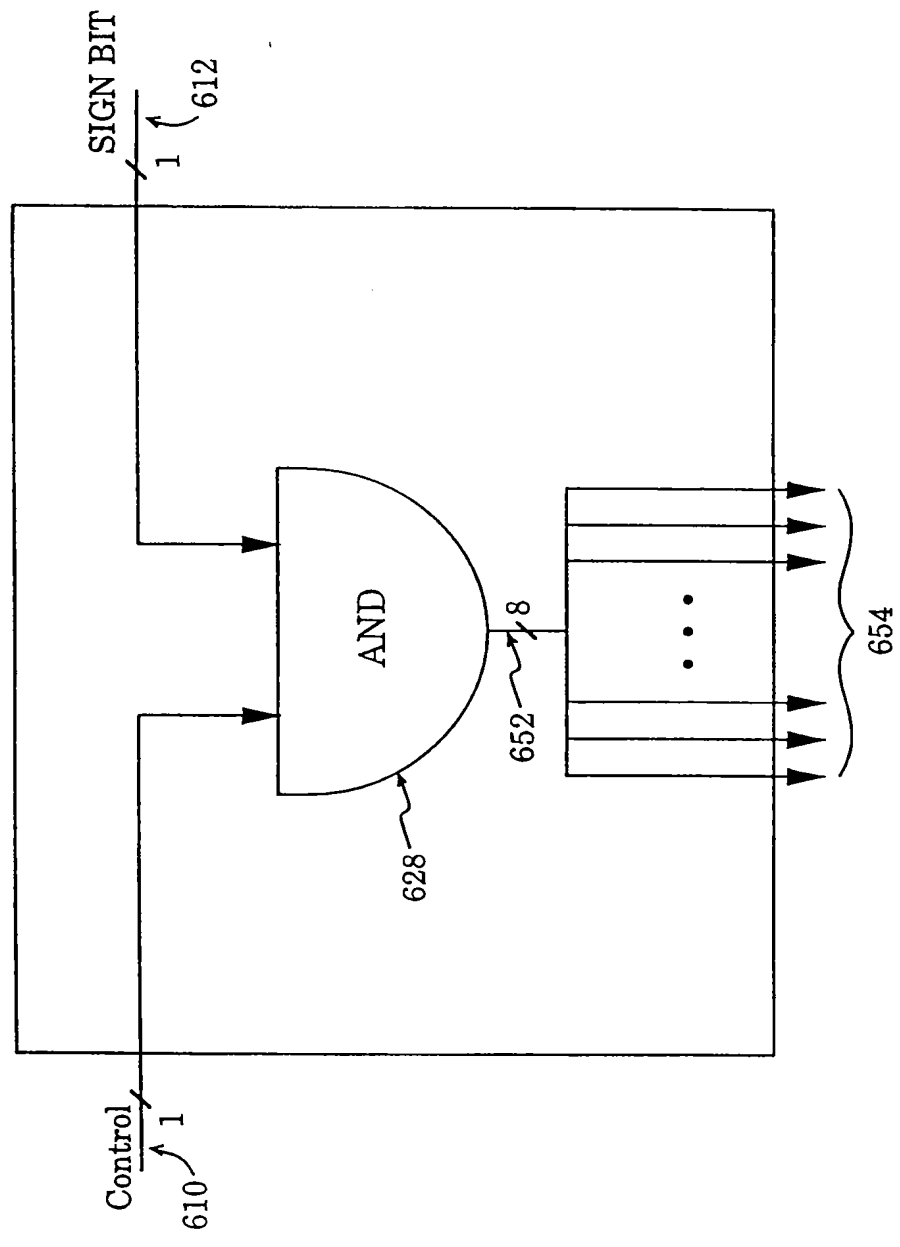


FIG. 6B

vd[7]	vd[6]	vd[5]	vd[4]	vd[3]	vd[2]	vd[1]	vd[0]	702
0	vs[7]	0	vs[6]	0	vs[5]	0	vs[4]	704
0	vs[3]	0	vs[2]	0	vs[1]	0	vs[0]	706
sign vs[7]	vs[7]	sign vs[6]	vs[6]	sign vs[5]	vs[5]	sign vs[4]	vs[4]	708
sign vs[3]	vs[3]	sign vs[2]	vs[2]	sign vs[1]	vs[1]	sign vs[0]	vs[0]	710
vs[7]	vs[5]	vs[3]	vs[1]	vt[7]	vt[5]	vt[3]	vt[1]	712
vs[6]	vs[4]	vs[2]	vs[0]	vt[6]	vt[4]	vt[2]	vt[0]	714
vs[7]	vt[7]	vs[6]	vt[6]	vs[5]	vt[5]	vs[4]	vt[4]	716
vs[3]	vt[3]	vs[2]	vt[2]	vs[1]	vt[1]	vs[0]	vt[0]	718

FIG. 7

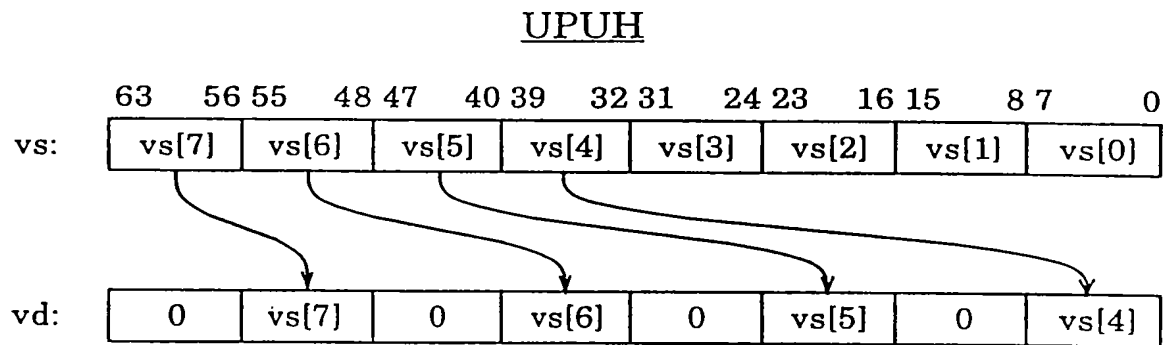


FIG. 8A

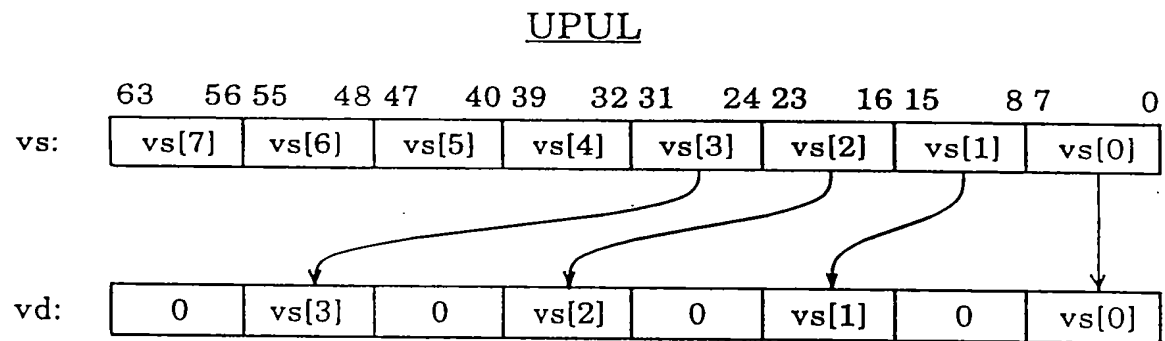


FIG. 8B

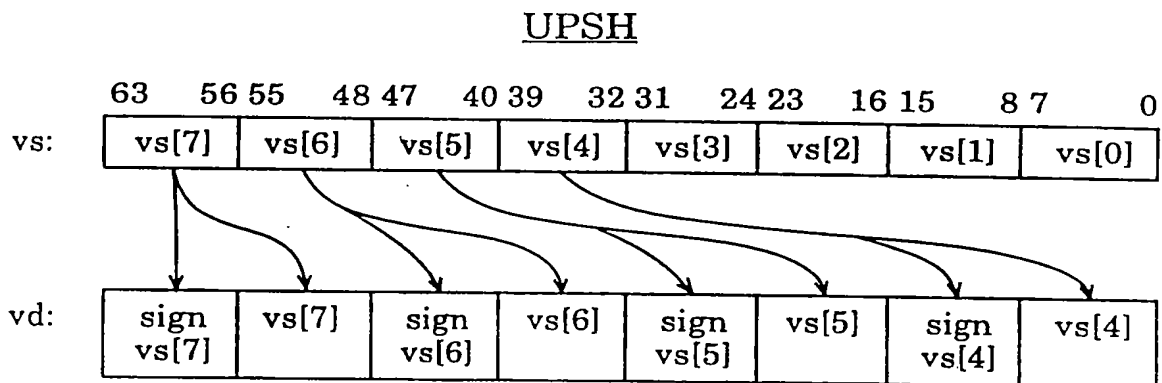


FIG. 8C

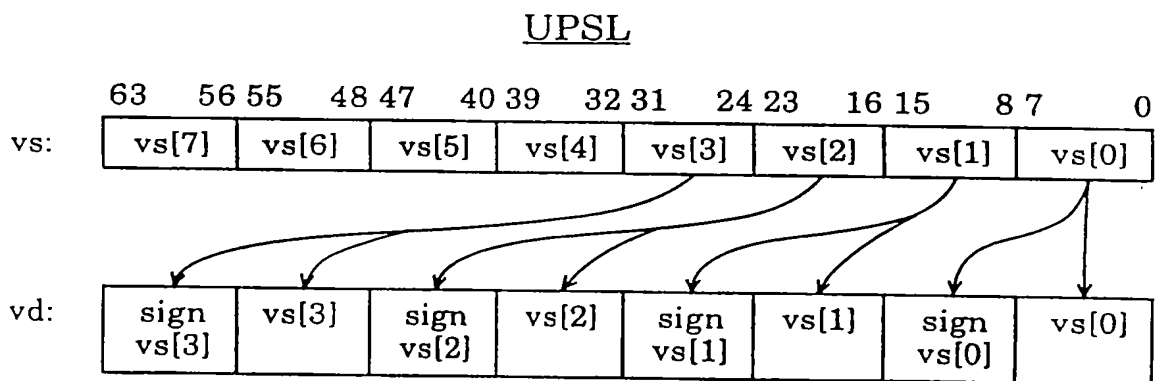


FIG. 8D

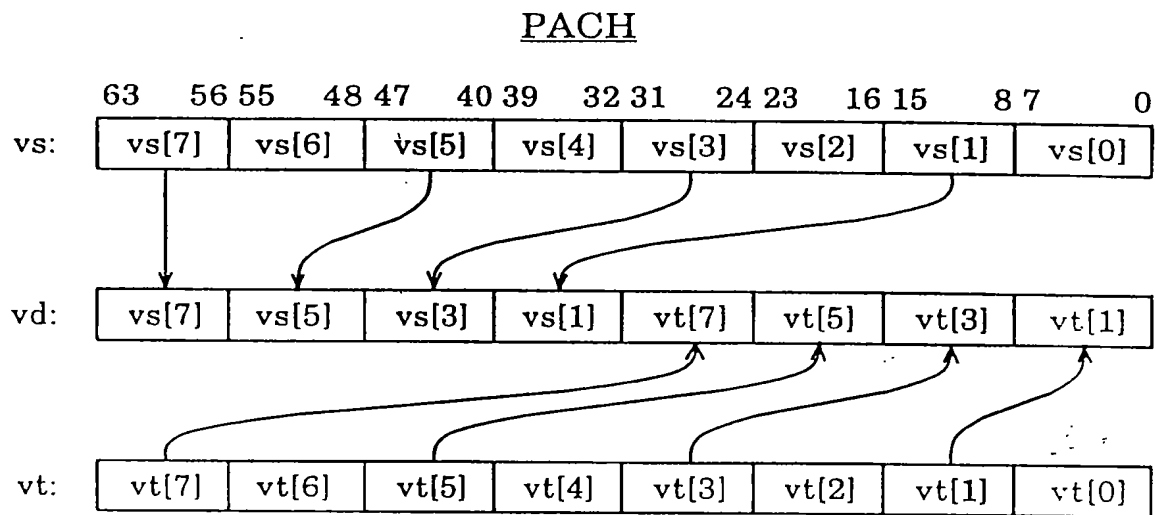


FIG. 8E

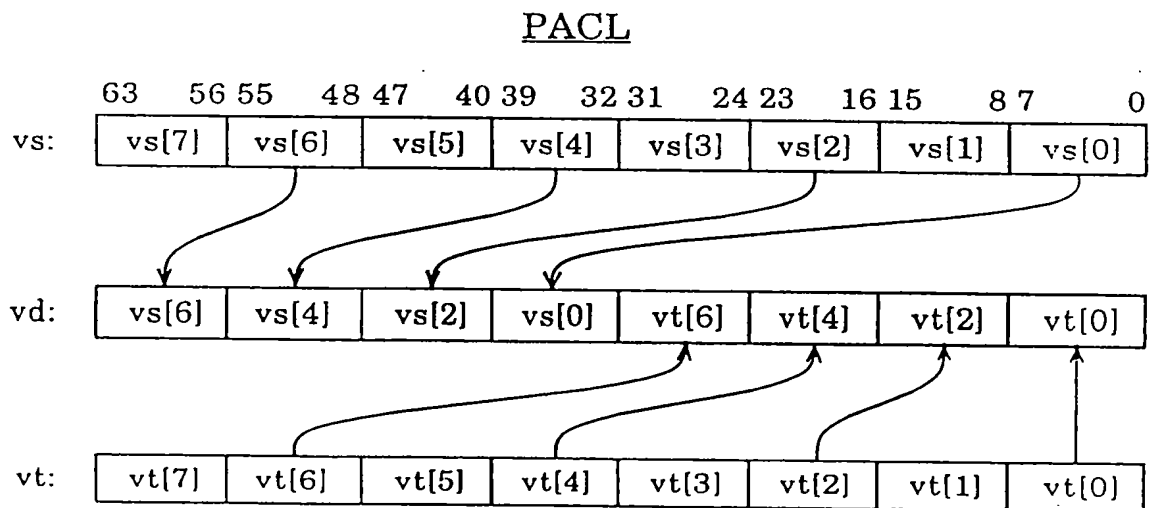


FIG. 8F

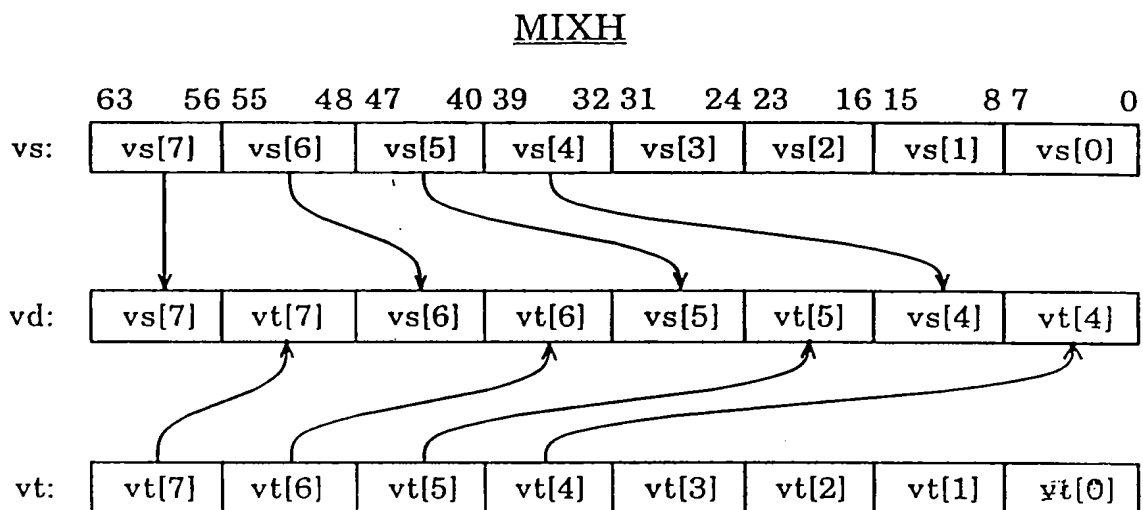


FIG. 8G

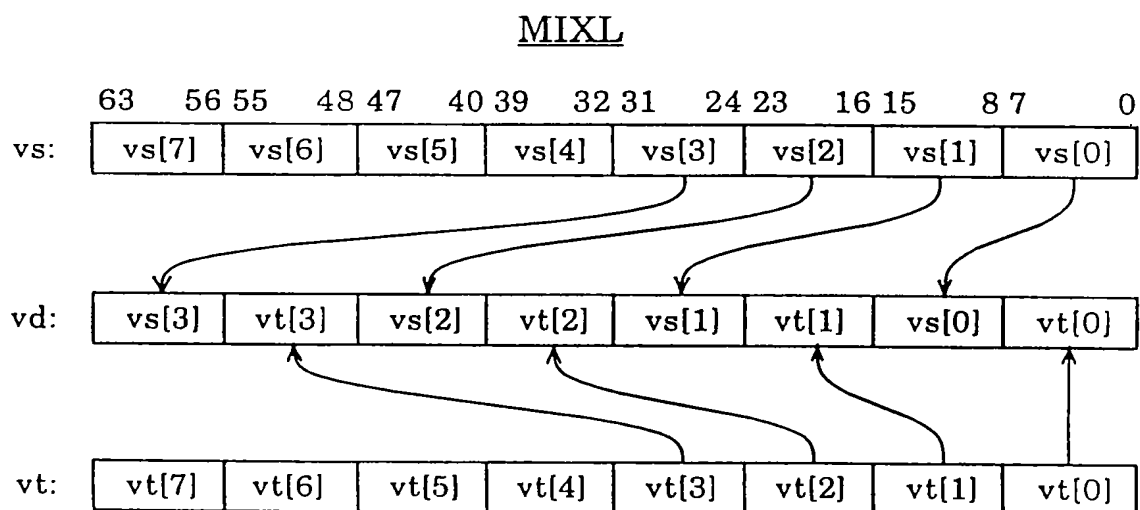


FIG. 8H

63				0
vd[3]	vd[2]	vd[1]	vd[0]	902
vs[3]	vt[3]	vs[2]	vt[2]	904
vs[1]	vt[1]	vs[0]	vt[0]	906
vs[3]	vs[1]	vt[3]	vt[1]	908
vs[2]	vs[0]	vt[2]	vt[0]	910
vs[2]	vt[3]	vs[0]	vt[1]	912
vs[0]	vt[1]	vs[2]	vt[3]	914
vs[3]	vs[2]	vt[3]	vt[2]	916
vs[1]	vs[0]	vt[1]	vt[0]	918

FIG. 9

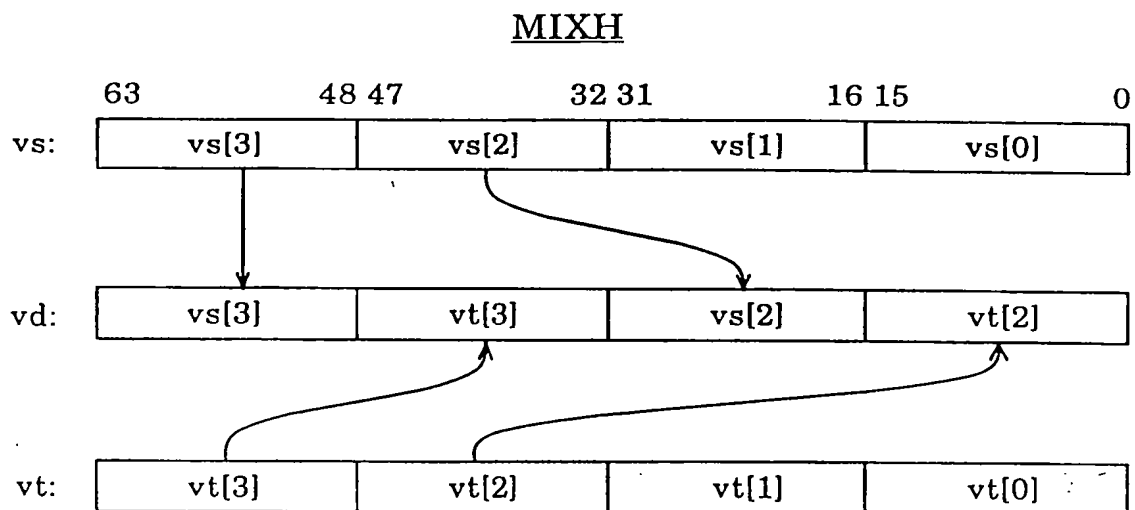


FIG. 10A

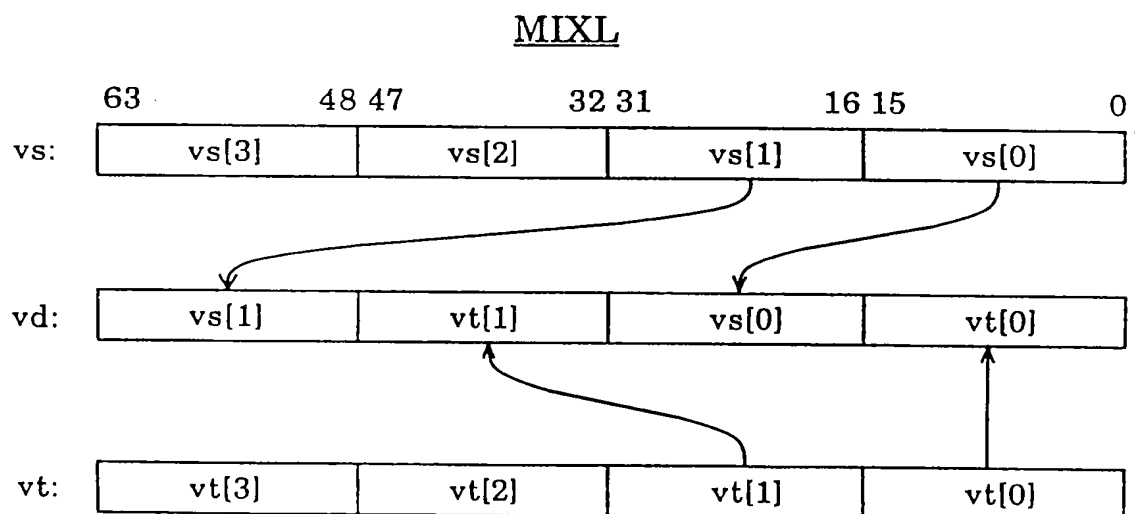


FIG. 10B

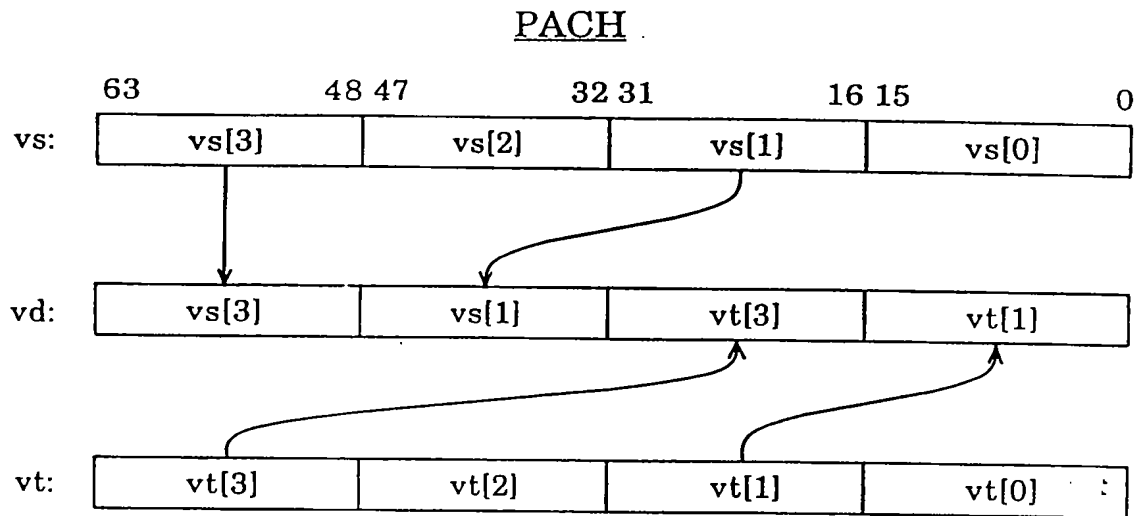


FIG. 10C

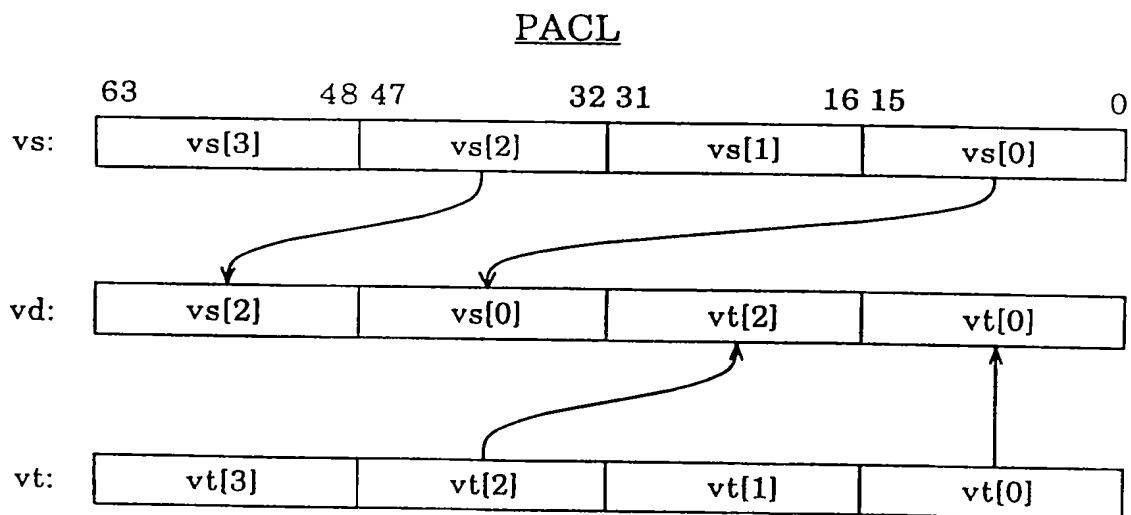


FIG. 10D

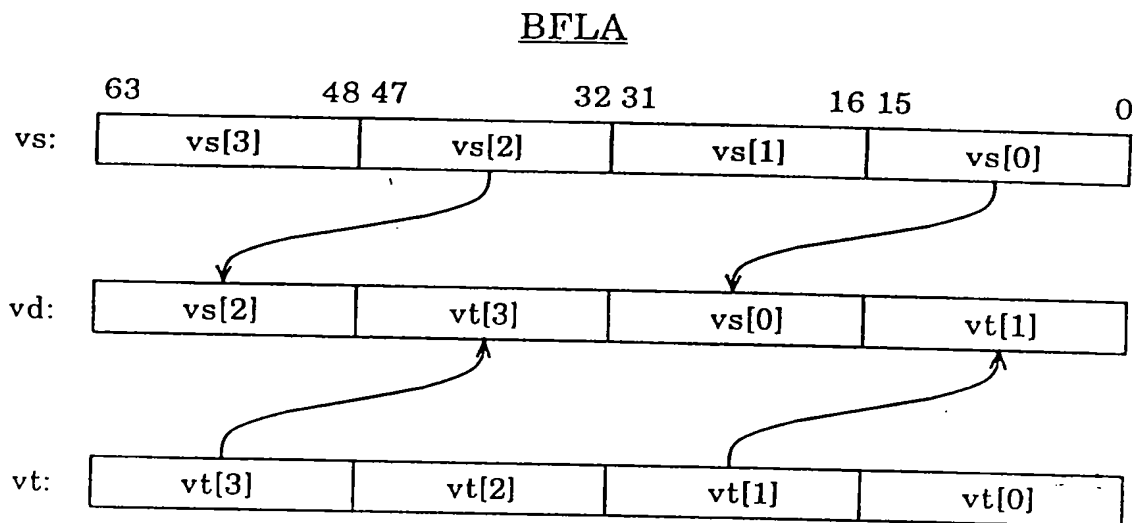


FIG. 10E

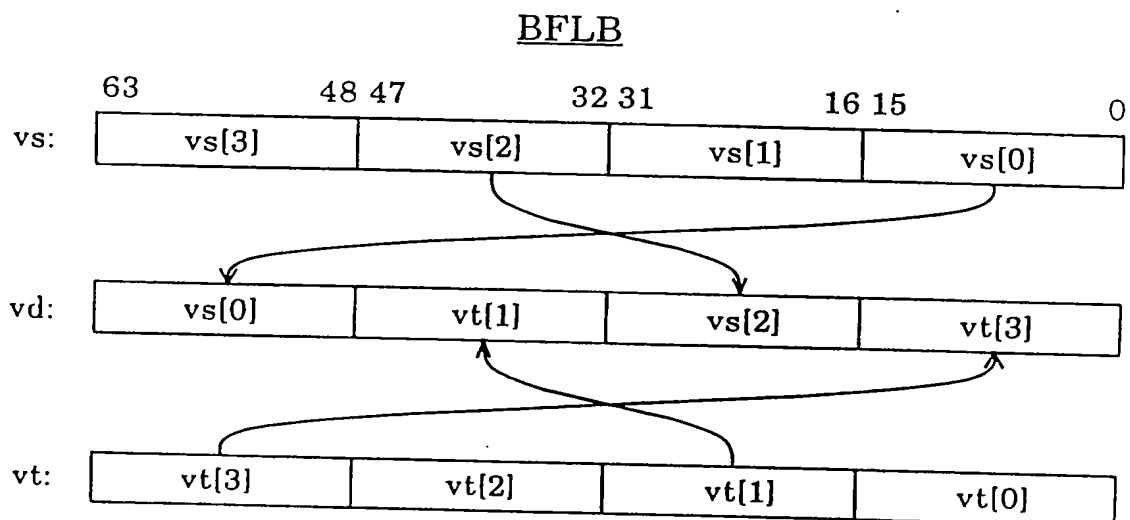


FIG. 10F

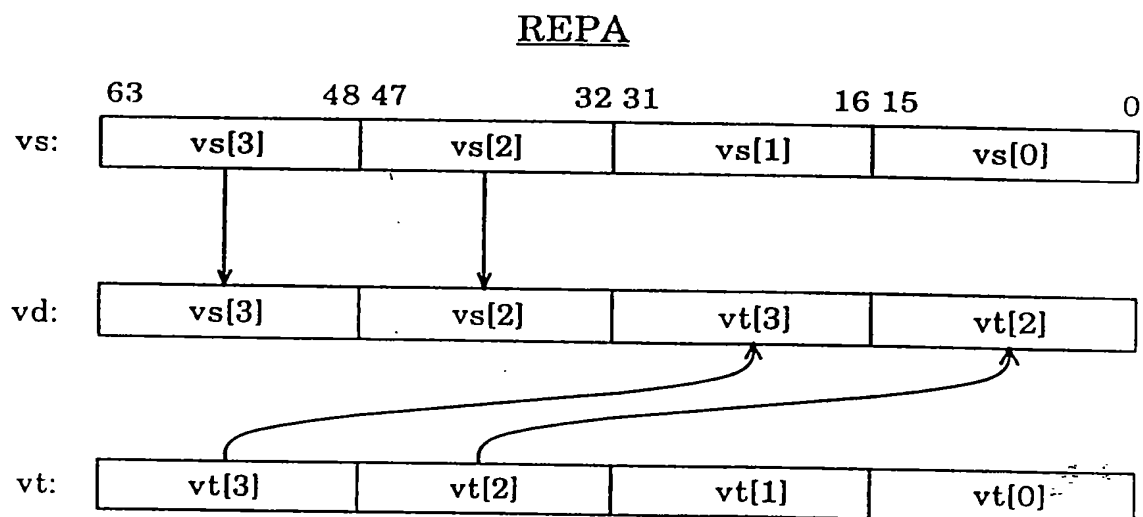


FIG. 10G

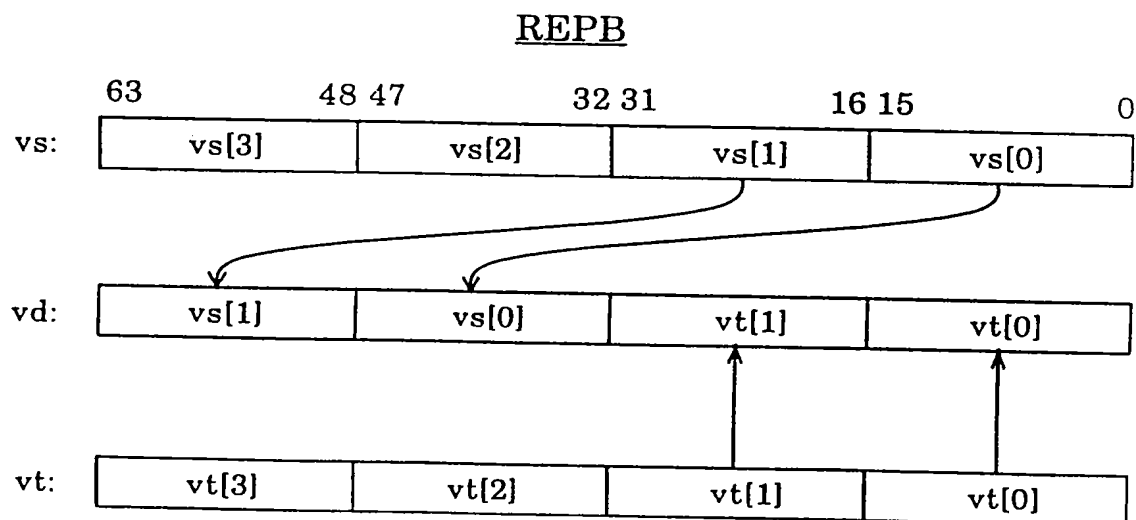


FIG. 10H